

DOCKET NO.: 246575US6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Takahito NAKANO, et al.

PATENT NO.: 7,346,712

GROUP: 2181

ISSUED: March 18, 2008

EXAMINER: FRANKLIN, RICHARD B

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS AND CIRCUIT
BOARD AND INFORMATION READOUT METHOD

REQUEST FOR CERTIFICATE OF CORRECTION

DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
ALEXANDRIA, VA 22313-1450

SIR:

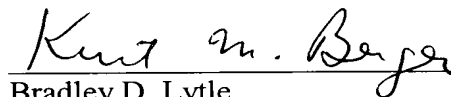
The following is a Request for Certificate of Correction in Serial Number 10/735,680,
now U.S. Patent Number 7,346,712.

In accordance with the provisions of Rule 322 of the Rules of Practice, which
implement 35 USC 254, the U.S. Patent and Trademark Office is respectfully requested to
issue a Certificate of Correction in the above-identified patent.

In light of the fact that the errors were the fault of the U.S. Patent and Trademark
Office, no fees are required. The requested corrections are listed on FORM P.T.O. 1050.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,346,712
DATED: March 18, 2008
INVENTOR(S): Takahito NAKANO, et al.

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10, line 5, insert --the-- before semiconductor.

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Patent No. 7,346,712

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